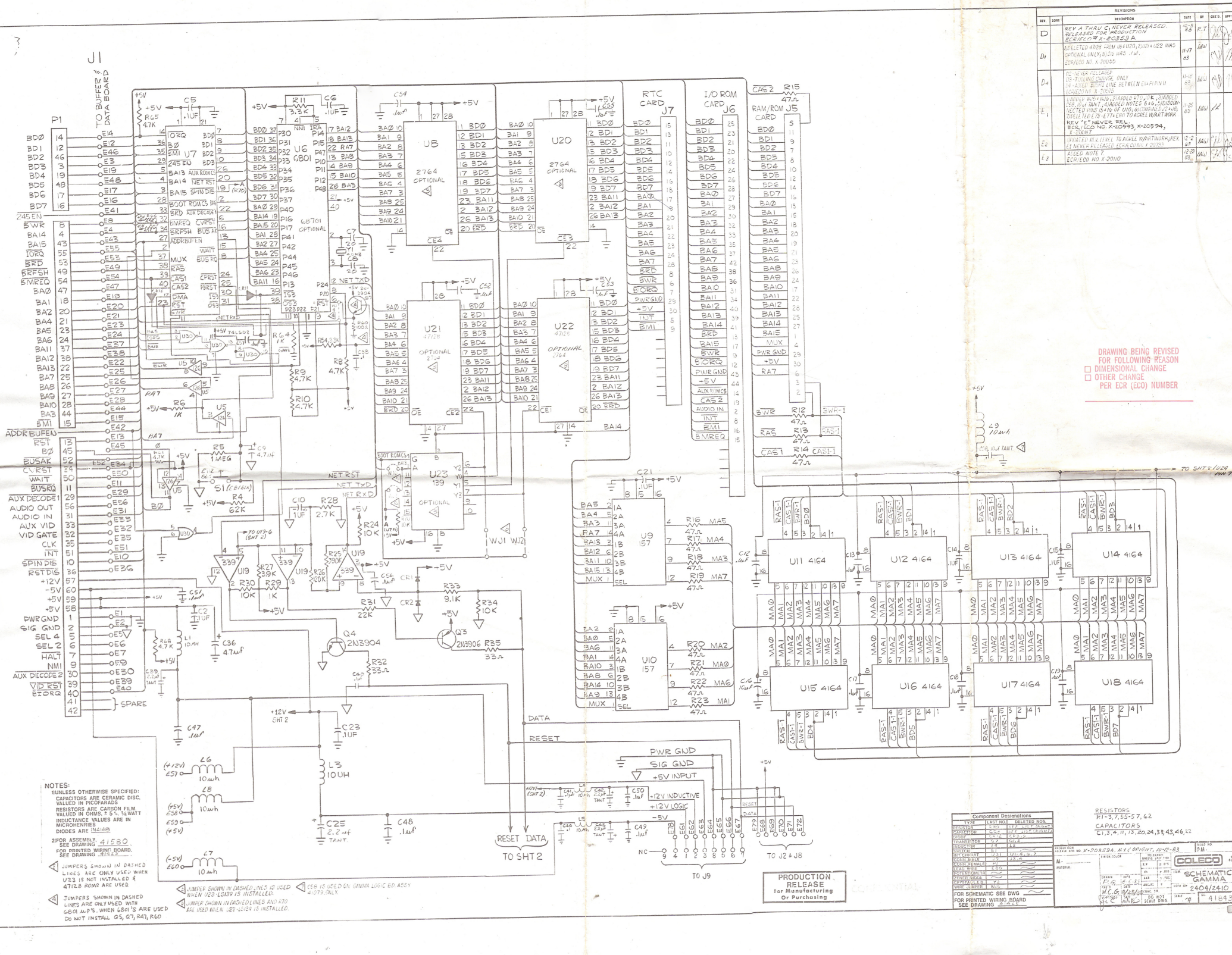
REVISIONS
REV. ZONE DESCRIPTION DATE BY CHK'D. APP'D.
D REV A THRU C1 NEVER RELEASED. RELEASED FOR PRODUCTION ECR/ECO# X-20353 A 10-25-83 R.T
D1 1)DELETED 4728 FROM U6 & U20; 2)U21 & U22 WAS OPTIONAL ONLY; 3) D16 WAS .1uf. ECR/ECO NO. X-20035 11-17-83
D4 R2 NEVER RELEASED DS-TOOLING CHANGE ONLY 14 - ADDED BRD*2 LINE BETWEEN EXAPIN H CONNECTOR & J1078 11-16-83
E1 1)ADDED R15 47Ω & U5 2)ADDED R12 47Ω, 3)ADDED Q5, 10uH TANT. 4)ADDED NOTES 5&6, 5)DISCONNECTED WIRE IS A1 OF U10; 6)COMBINED U2 & U8, DELETE U2 PER ECR 7) ADDED U19 & U20 REV "E" NEVER REL., ECR/ECO NO. X-20593, X-20594, X-20617 11-25-83
E2 1)UPDATED REV. LEVEL TO AGREE W/ARTWORK REV. E1 NEVER RELEASED ECR# QUINN X 20705 12-2-83
E3 ADDED NOTE 7 ECR/ECO NO. X-20110 12-28-83

DRAWING BEING REVISED FOR FOLLOWING REASON
☐ DIMENSIONAL CHANGE
☐ OTHER CHANGE PER ECR (ECO) NUMBER

J1 TO BUFFER DATA BOARD
P1
BDØ 14
BD1 12
BD2 46
BD3 3
BD4 19
BD5 48
BD6 17
BD7 16
245 EN 8
BWR 4
BA14 43
BA15 55
IORQ 53
BRD 49
BRFSH 54
BMREQ 47
BAØ 18
BA1 20
BA2 21
BA3 23
BA4 24
BA5 37
BA6 38
BA7 22
BA8 25
BA9 26
BA10 27
BA11 28
BA12 44
BA13 15
ADDR BUFEN 13
RST 45
BØ 52
BUSAK 24
CLK RST 50
WAIT 11
BUSRQ 29
AUX DECODE1 56
AUDIO OUT 31
AUDIO IN 33
AUX VID 32
VID GATE 35
CLK 51
INT 10
SPIN DIS 36
RST DIS 57
+12V 60
-5V 59
+5V 58
PWR GND 1
SIG GND 2
SEL 4 5
SEL 2 6
HALT 7
NMI 9
AUX DECODE2 30
VID RST 39
BIORQ 40
SPARE 41 42
U7 Z80A CPU
U6 68701 OPTIONAL
U8 2764 OPTIONAL
U20 2764 OPTIONAL
U21 47128 OPTIONAL 2764
U22 47128 OPTIONAL 2764
U23 139 OPTIONAL
U9 157
U10 157
U11 4164
U12 4164
U13 4164
U14 4164
U15 4164
U16 4164
U17 4164
U18 4164
U19 339
U5 74LS02
U30 74LS08
RTC CARD J7
I/O ROM CARD J6
RAM/ROM CARD J5
CAS2 R15 47Ω
R12 47Ω
R13 47Ω
R14 47Ω
L9 10uH TANT.
TO SHT 2 /J29
R1 3.3K
R2 4.7K
R3 4.7K
R4 62K
R5 1MEG
R6 1K
R7 1K
R8 4.7K
R9 4.7K
R10 4.7K
R11 3.3K
R24 10K
R25 390K
R26 120K
R27 89K
R28 2.7K
R29 1K
R30 10K
R31 22K
R32 33Ω
R33 9.1K
R34 10K
R35 33Ω
R54 33K
R68 4.7K
R69 2.2K
C1 .1uF
C2 .1UF
C5 .1UF
C6 .1UF
C7 20
C8 20
C9 4.7uF
C10 .1uF
C21 .1UF
C22 .1uF
C23 .1UF
C25 2.2uf TANT.
C36 4.7uF
C47 .1uF
C48 .1uF
C49 .1uF
C50 .1uF
L1 10MH
L3 10UH
L6 10uH
L7 10uH
L8 10uH
Q3 2N3906
Q4 2N3904
Y1 4.0MHz
Y2 4.0MHz
S1 (E18 IN)
WJ1 WJ2
NET RST
NET TXD
NET RXD
DATA
RESET
PWR GND
SIG GND
+5V INPUT
+12V INDUCTIVE
+12V LOGIC
-5V
TO J9
TO J2 & J8
TO SHT 2
NOTES:
1 UNLESS OTHERWISE SPECIFIED: CAPACITORS ARE CERAMIC DISC. VALUED IN PICOFARADS RESISTORS ARE CARBON FILM VALUED IN OHMS, ± 5%, ¼ WATT INDUCTANCE VALUES ARE IN MICROHENRIES DIODES ARE IN4148
2 FOR ASSEMBLY, SEE DRAWING 41580. FOR PRINTED WIRING BOARD, SEE DRAWING 41843
3 JUMPERS SHOWN IN DASHED LINES ARE ONLY USED WHEN U23 IS NOT INSTALLED & 47128 ROMS ARE USED
4 JUMPERS SHOWN IN DASHED LINES ARE ONLY USED WITH 6801 MP'S. WHEN 6801 'S ARE USED DO NOT INSTALL G5, G7, R47, R60
5 JUMPER SHOWN IN DASHED LINES IS USED WHEN U23-LS139 IS INSTALLED.
6 JUMPER SHOWN IN DASHED LINES AND R70 ARE USED WHEN U23-LS139 IS INSTALLED.
7 C68 IS USED ON GAMMA LOGIC ED. ASSY 41079 ONLY.
PRODUCTION RELEASE for Manufacturing Or Purchasing
Component Designations
TYPE | LAST NO | DELETED NOS.
RESISTOR R70 | R71
CAPACITOR C67 | C68
DIODE CR15 | CR16
TRANSISTOR Q4 | Q5
INDUCTOR L8 | L9
SWITCH S1 | S2
INT. CIRCUIT U23 | U24
CONN. MALE J7 | J8
CONN. FEMALE P1 | P2
LEAD WIRE E80 | E81
POTENT. OHMTR R11 | R12
ZENER DIODE VR | VR2
CRYSTAL (OSC.) Y2 | Y3
WIRE JUMPER W16 | W17
FOR SCHEMATIC SEE DWG
FOR PRINTED WIRING BOARD SEE DRAWING 41843
PRODUCTION RELEASE ECO NO. X-20359A, N.Y.C. DEVENT, 10-12-83
RESISTORS R1-3,7,55-57,62
CAPACITORS C1,3,4,11,13,20,24,33,43,46,22
M - MATERIAL
FINISH-COLOR
TOLERANCE UNLESS SPEC'FIED
X ± .1 .X ± .03
XX ± .03
ANGLES ± 1°
DRAWN E.G.S. 8/25/83
CHK'D N.C.G. 8/25/83
ENGR'G APP'L C
DO NOT SCALE DWG.
COLECO
ITEM SCHEMATIC GAMMA
SIZE D
DWG NO 2404/2410
REV E3
SCALE NONE
SHEET 1 OF 1